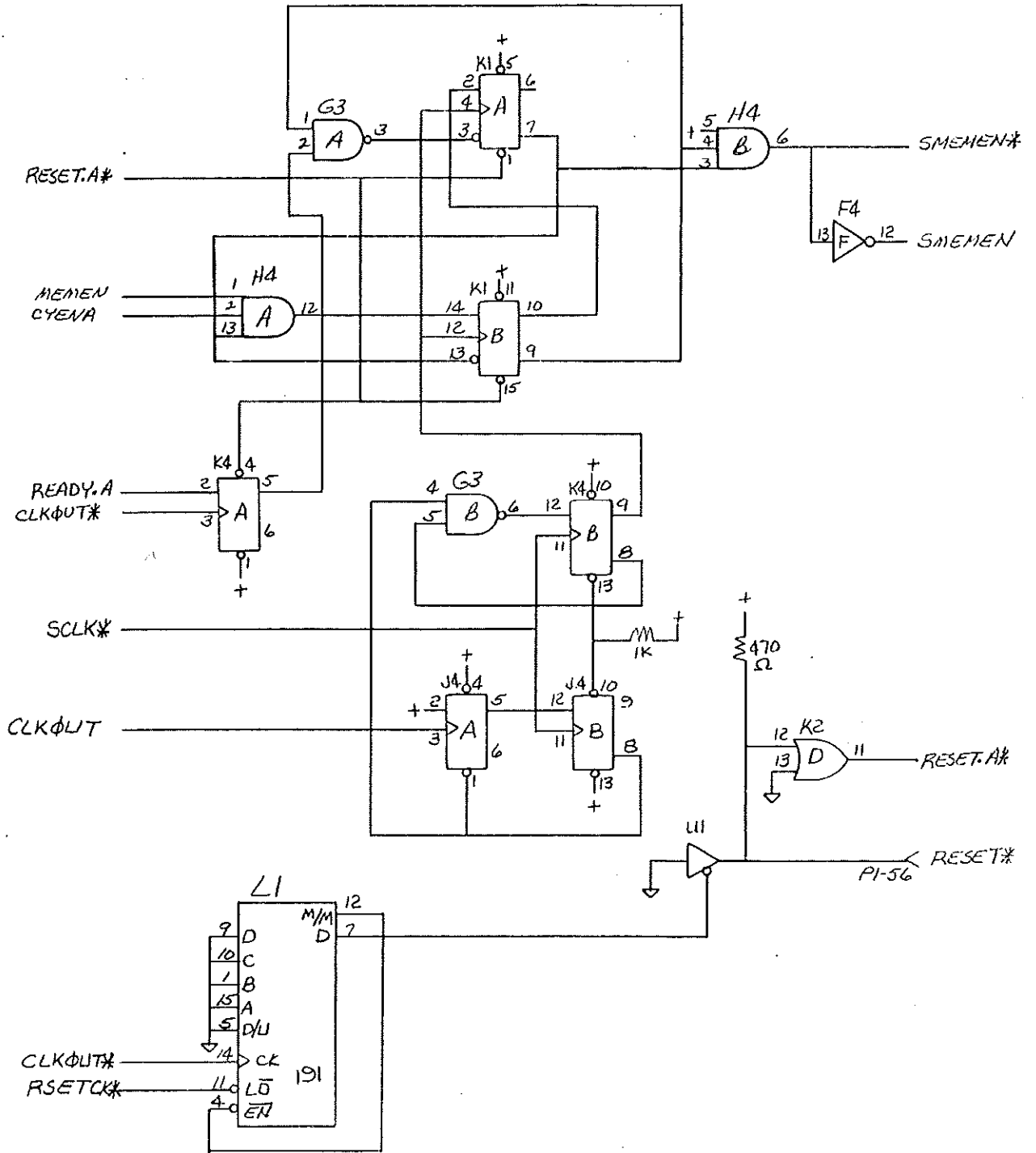
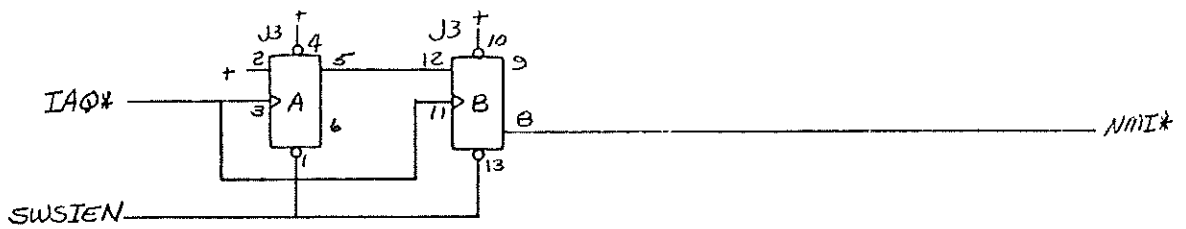


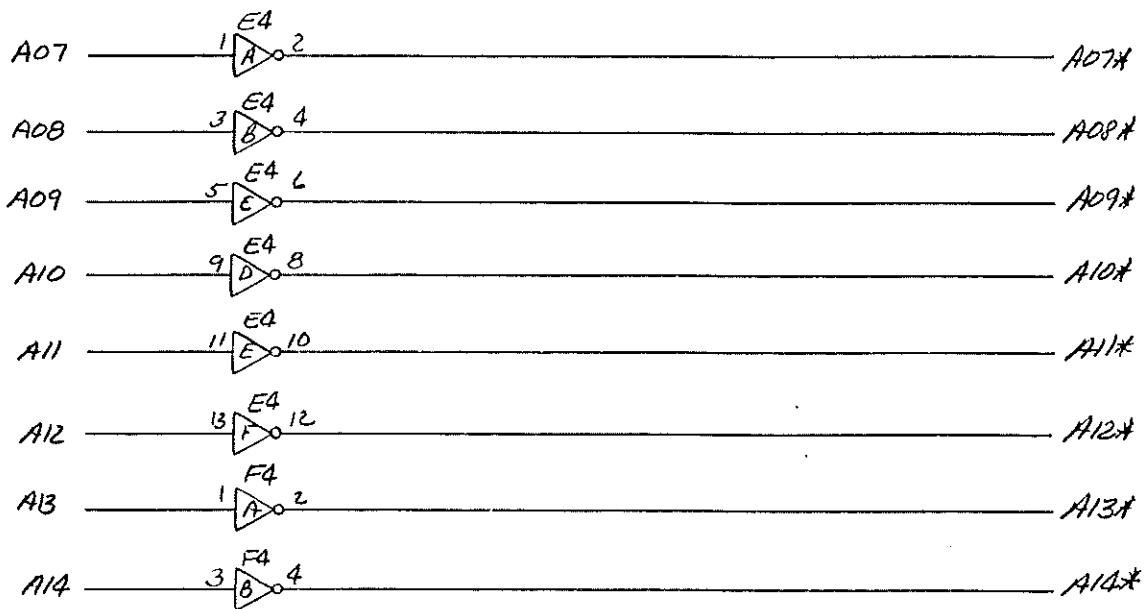
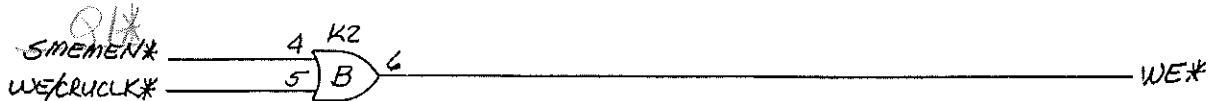
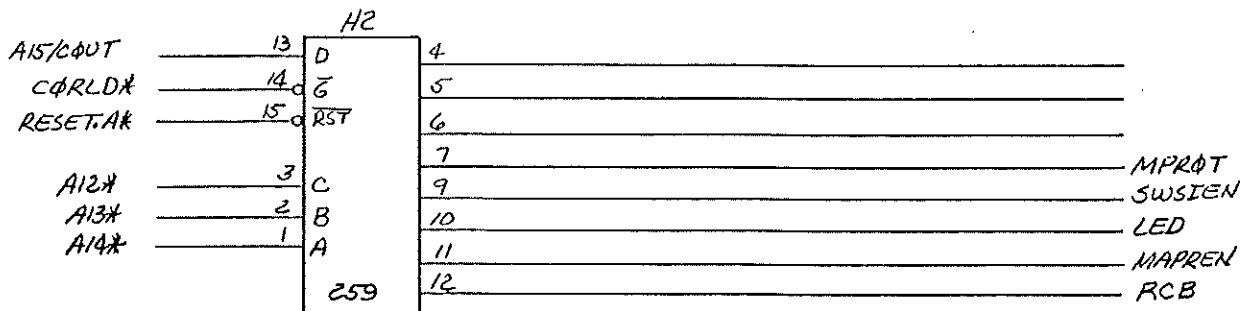
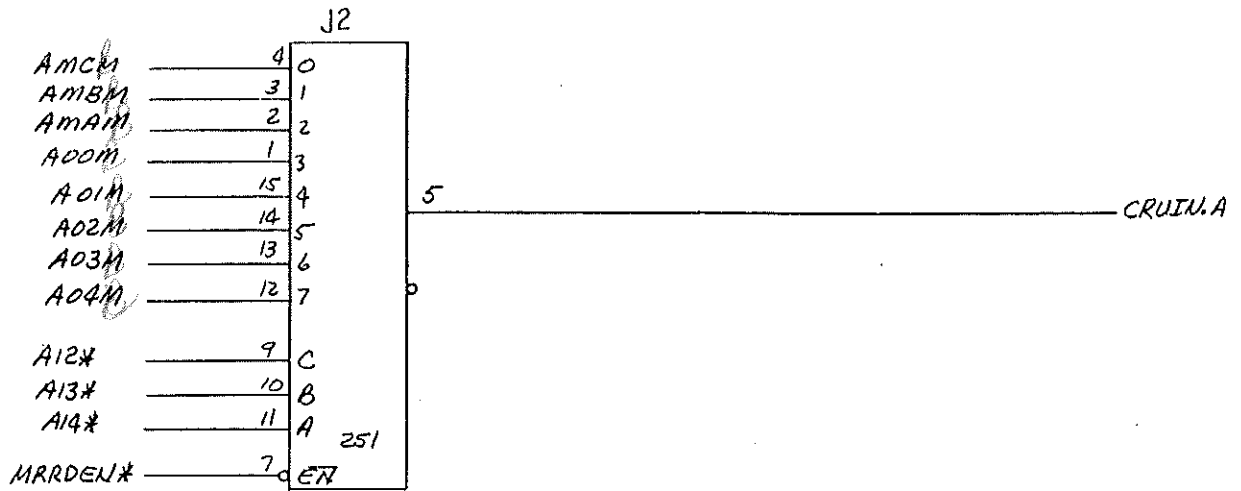
11/2/21

MP, Xtal Osc

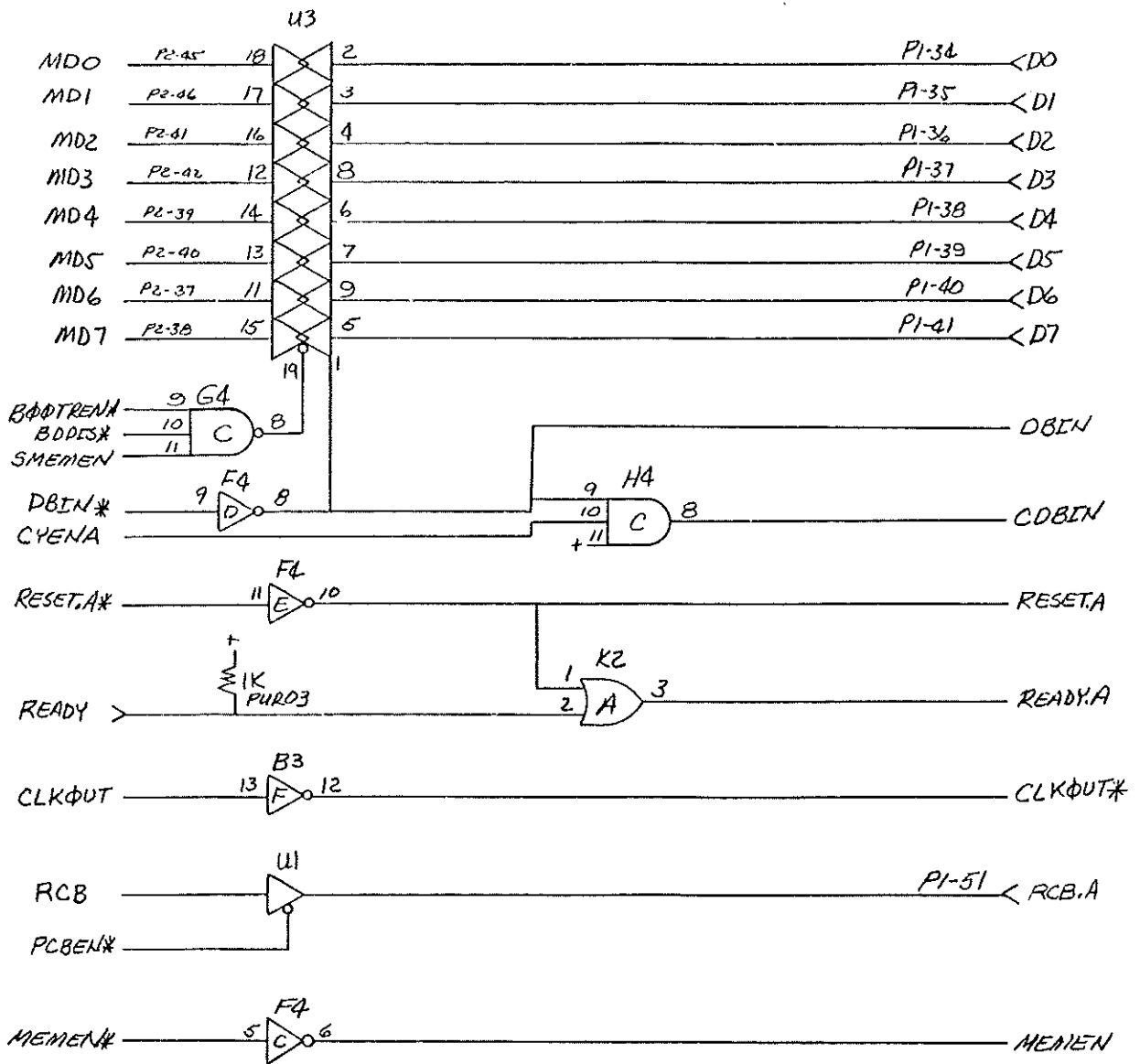


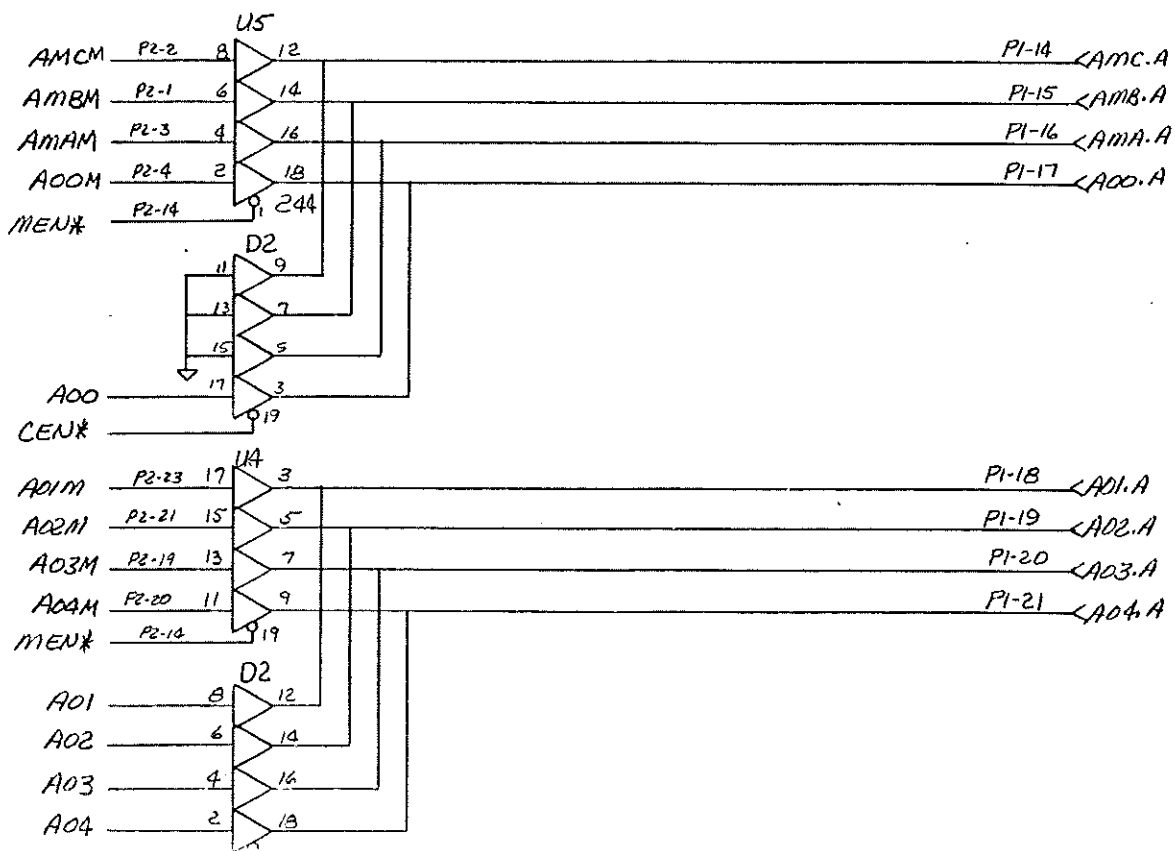
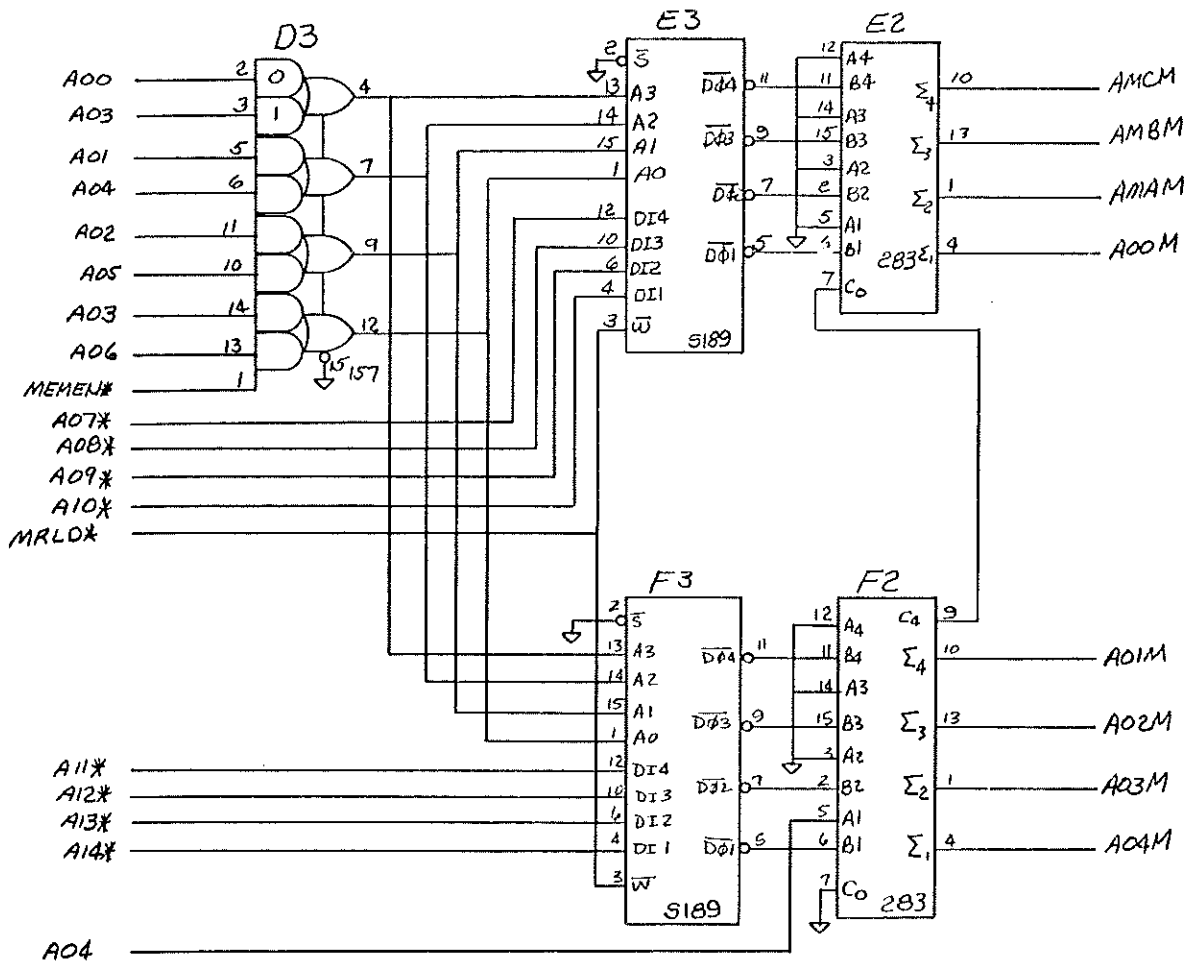
CPU

Single Instruct, MEMEN Control, SW Reset



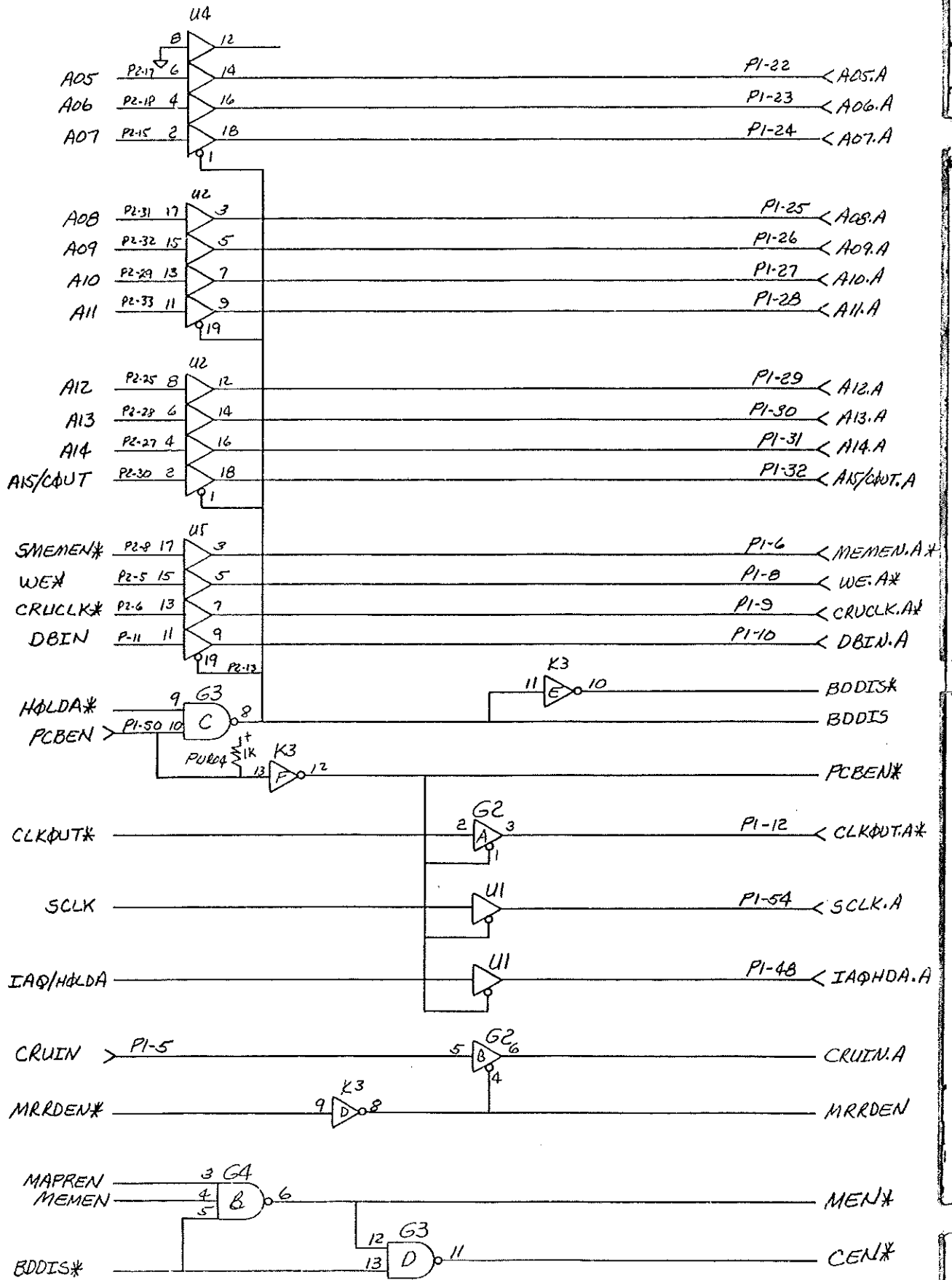
CRU IN, OUT Reg, WE decode, A11-14 Inverters





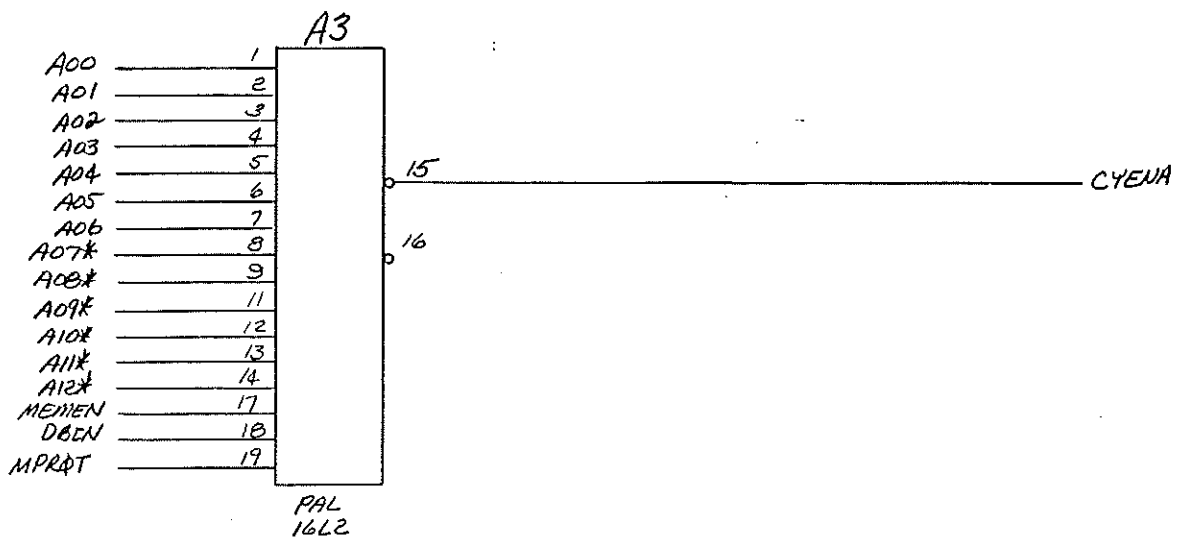
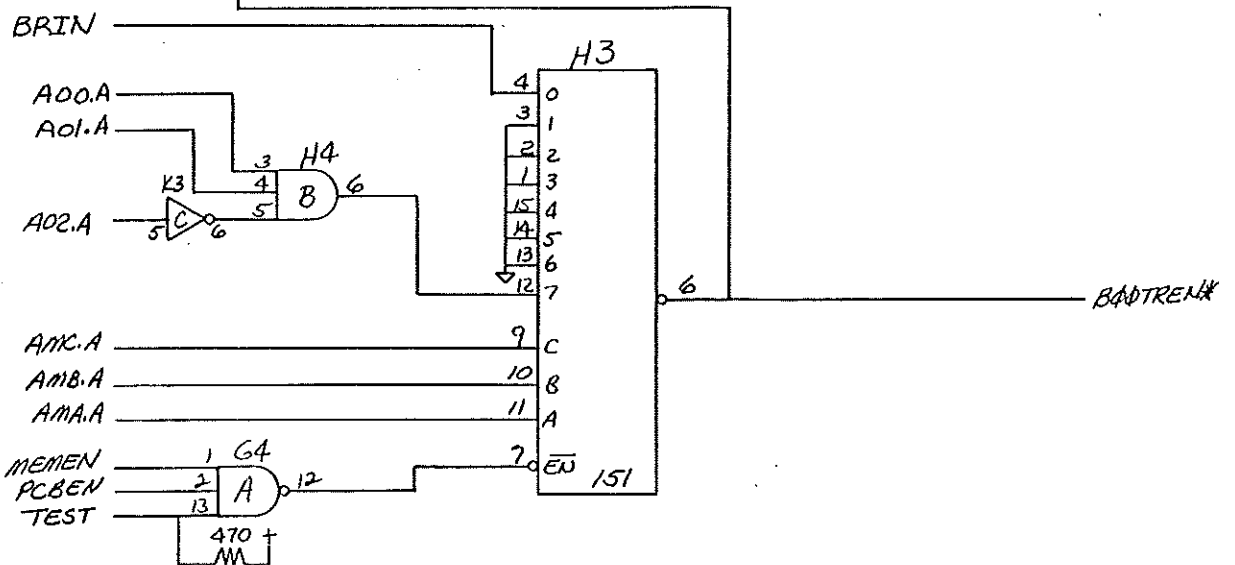
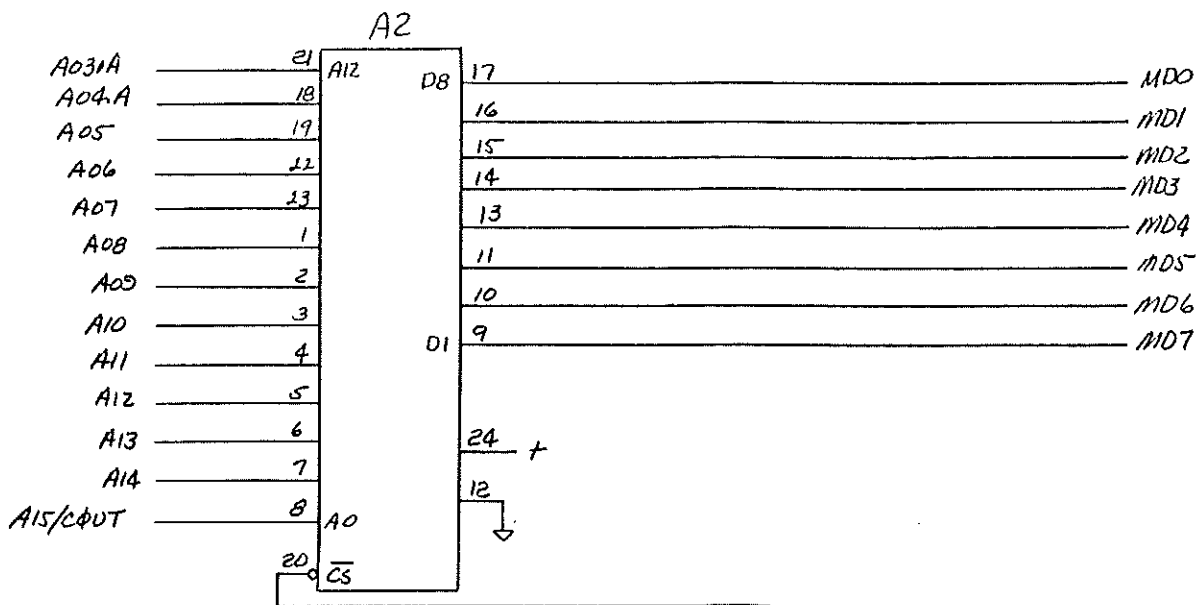
CPU

Mapper + Mapper/CPU Address Drivers



U4

Address, Control Bus Buffers, Mapper Output Control



RAM + CS Control, PLA

